Forschungsberichte aus dem Ferdinand-Braun-Institut Leibniz-Institut für Höchstfrequenztechnik AlGaN/GaN HEMTs Reliability: Degradation Modes and Analysis

AlGaN/GaN HEMTs Reliability

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Reviews

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(Prof. Loyce Runolfsson Jr.)

ALGAN/GAN HEMTS RELIABILITY



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Cuvillier Verlag Okt 2012, 2012. Taschenbuch. Book Condition: Neu. 211x147x10 mm. Neuware - AlGaN/GaN HEMTs reliability and stability issues were investigated in dependence on epitaxial design and process modification. DC-Step-Stress-Tests have been performed on wafers as a fast device robustness screening method. As a criterion of robustness they deliver a critical source-drain voltage for the onset of degradation. Several degradation modes were observed which depend on epi design, epi quality and process technology. Electrical and optical characterizations together with electric field simulations were performed to get insight into respective degradation modes. It has been found that AlGaN/GaN HEMT devices with GaN cap show higher critical source-drain voltages as compared to non-capped devices. Devices with low Al concentration in the AlGaN barrier layer also show higher critical source-drain voltages. Superior stability and robustness performance have been achieved from devices with AlGaN backbarrier epi design grown on n-type SiC substrate. For the onset on any degradation modes the presence of high electrical fields is most decisive for ONand OFF-state operation conditions. Therefore careful epi design to reduce high electric field is mandatory. It is also shown that epi buffer quality and growth process have a great impact on device robustness. Defects such as point defects and dislocations are assumed to be created initially during stressing and accumulated to larger defect clusters during device stressing. Electroluminescence (EL) measurements were performed to detect early degradation. Extended localized defects are resulting as bright spots at OFF-state conditions in conjunction with a gate leakage increase.AlGaN/GaN HEMTs mit unterschiedlichen epitaktischen Designs und Prozessmodifikationen wurden auf ihre Zuverlässigkeit und Stabilität untersucht. DC-Stufenstresstests wurden als Screeningmethode für die Bauelementrobustheit durchgeführt. Mit dieser Methode erhält man eine kritische Source-Drain-Spannung, die den Beginn der Degradation kennzeichnet. Verschiedene Degradationsmodi wurden beobachtet, die vom epitaxialem Design, der epitaxialen Qualität und der Prozesstechnologie...

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